

S/109/672,711

PATENT

FEB 24 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Thomas E. Burton et al. Examiner: Larry Donaghue
Serial No.: 09/672,711 Group Art Unit: 2154
Filed: September 29, 2000 Docket: 884.984US1
Title: HOST-FABRIC ADAPTER HAVING HARDWARE ASSIST ARCHITECTURE
AND METHOD OF CONNECTING A HOST SYSTEM TO A CHANNEL-
BASED SWITCHED FABRIC IN A DATA NETWORK
Assignee: Intel Corporation Customer Number: 21186

INFORMATION DISCLOSURE STATEMENT

MS RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. § 1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement.

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Assignee: Intel Corporation

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The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications are no longer required to be provided to the Office. Notification of this change was provided in the United States Patent and Trademark Office OG Notices dated October 12, 2004. Thus, Applicant has not included copies of any US Patents or Published Applications cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

Respectfully submitted,

THOMAS E. BURTON ET AL.

By their Representatives,

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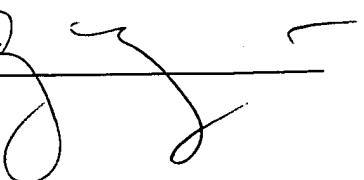
By Robert Madden

Robert Madden

Reg. No. 57,521

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 21st day of February, 2006.

Amy Moriarty
Name


Signature

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary) 		Complete if Known Application Number 09/672,711 Filing Date September 29, 2000 First Named Inventor Burton, Thomas Group Art Unit 2154 Examiner Name Donaghue, Larry	
Sheet 1 of 1		Attorney Docket No: 884.984US1	

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-20030091037A1	05/15/2003	Latif, A. , et al.	04/30/2002
	US-6,778,548	08/17/2004	Burton, T. E., et al.	06/26/2000
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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		BEGEL, A. , et al., "An Analysis of VI Architecture Primitives in Support of Parallel and Distributed Communication", <u>Concurrency and Computation: Practice and Experience</u> , 14(1), (2001),1-16	
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		MAHON, M. J., et al., "Hewlett-Packard Precision Architecture: The Processor", <u>Hewlett-Packard Journal</u> , (Aug. 1986),4-22	
		PATEL, N. , et al., "A model of completion queue mechanisms using the virtual interface API", <u>IEEE International Conference on Cluster Computing</u> , 2000. Proceedings., (2000),280-288	

EXAMINER

DATE CONSIDERED